

- TRANSMISSION BIT ORDER (D0 TO D9 SHOW TRANSMITTED DATA, C4 TO C0 SHOW CRC BITS)
- CONVENTIONAL POSTPOSITION : D9,D8,D7,D6,D5,D4,D3,D2,D1,D0,C4,C3,C2,C1,C0
- PREPOSITION : C4,C3,C2,C1,C0,D9,D8,D7,D6,D5,D4,D3,D2,D1,D0

FIG. 1A

- RECEIVED DATA BIT AND RECEIVED CRC BIT
 (WHEN DETECTING A POSITION WHERE THE NUMBER OF BITS IS
 SMALLER BY ONE FROM THE CORRECT RATE POSITION)

CONVENTIONAL POSTPOSITION: DATA = D9 , D8 , D7 , D6 , D5 , D4 , D3 , D2 , D1 CRC=D0 , C4 , C3 , C2 , C1

PREPOSITION: DATA = D9 , D8 , D7 , D6 , D5 , D4 , D3 , D2 , D1 CRC=C4 , C3 , C2 , C1 , C0

FIG. 1B

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○TRANSMISSION BIT ORDER (D0 TO D9 SHOW TRANSMITTED DATA, C4 TO C0 SHOW CRC BITS)

CONVENTIONAL POSTPOSITION : D9,D8,D7,D6,D5,D4,D3,D2,D1,D0,C4,C3,C2,C1,C0

NEW POSTPOSITION : D9,D8,D7,D6,D5,D4,D3,D2,D1,D0,C0,C1,C2,C3,C4

FIG.2A

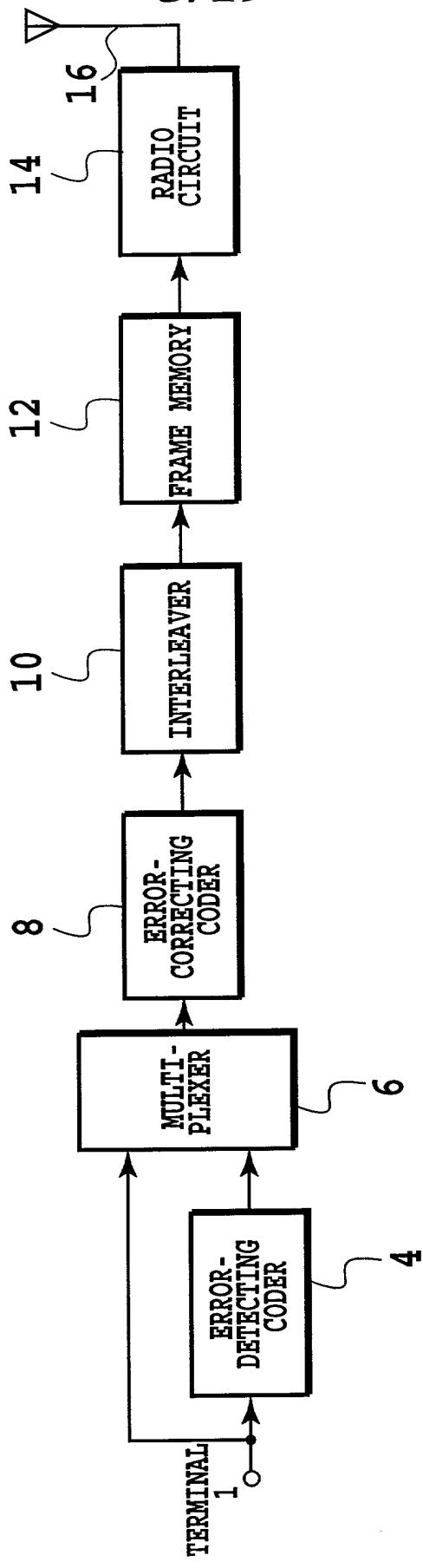
○RECEIVED DATA BIT AND RECEIVED CRC BIT
(WHEN DETECTING A POSITION WHERE THE NUMBER OF BITS IS
SMALLER BY ONE FROM THE CORRECT RATE POSITION)

CONVENTIONAL POSTPOSITION: DATA =D9,D8,D7,D6,D5,D4,D3,D2,D1 CRC=D0,C4,C3,C2,C1

NEW POSTPOSITION: DATA =D9,D8,D7,D6,D5,D4,D3,D2,D1 CRC=D0,C0,C1,C2,C3

FIG.2B

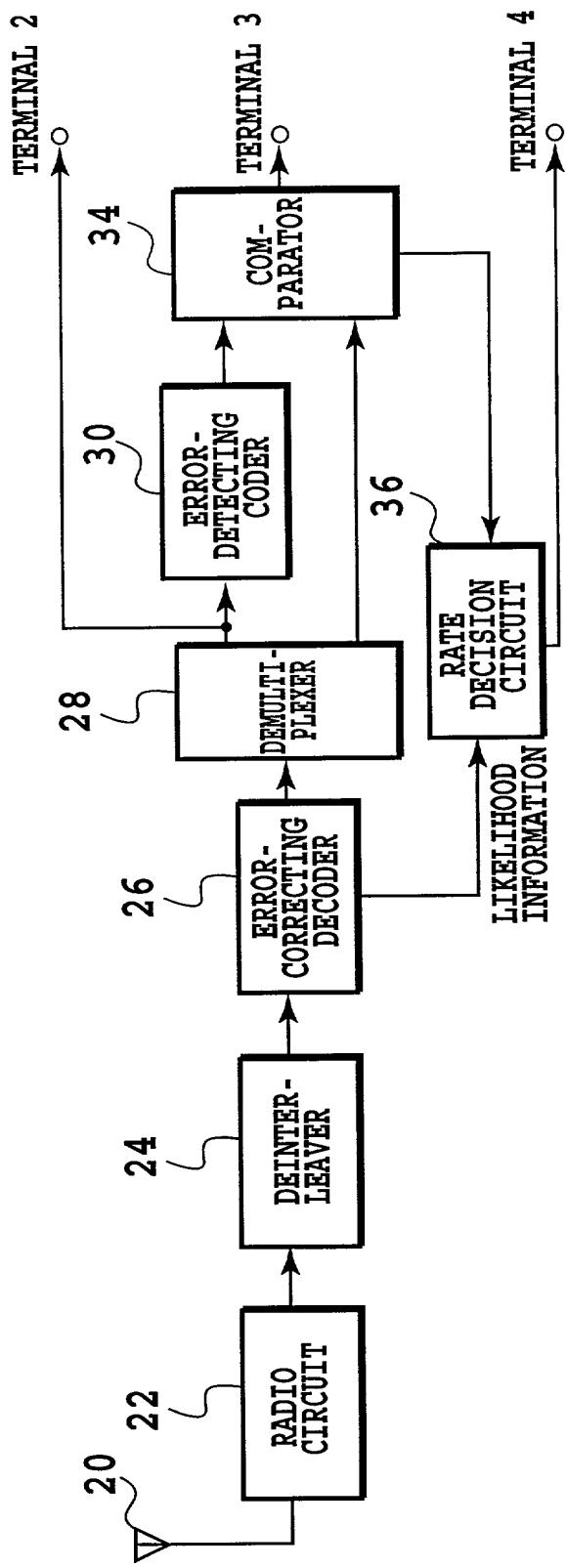
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TRANSMITTER CONFIGURATION

FIG.3A

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RECEIVER CONFIGURATION

FIG.3B

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OUTPUT OF MULTIPLEXER

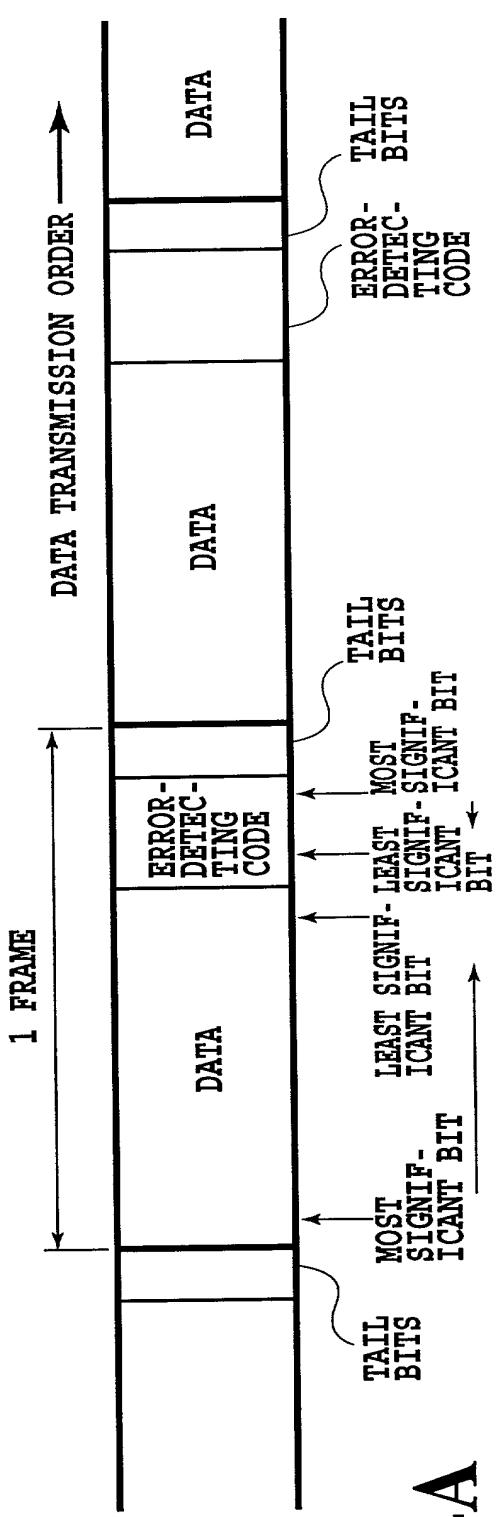


FIG.4A

OUTPUT OF MULTIPLEXER

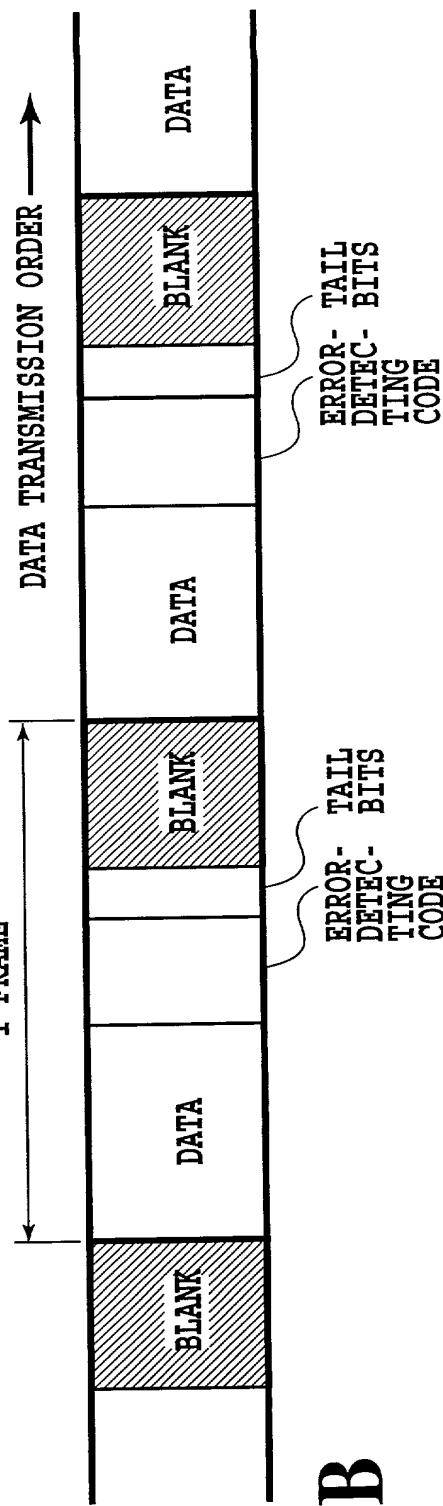


FIG.4B

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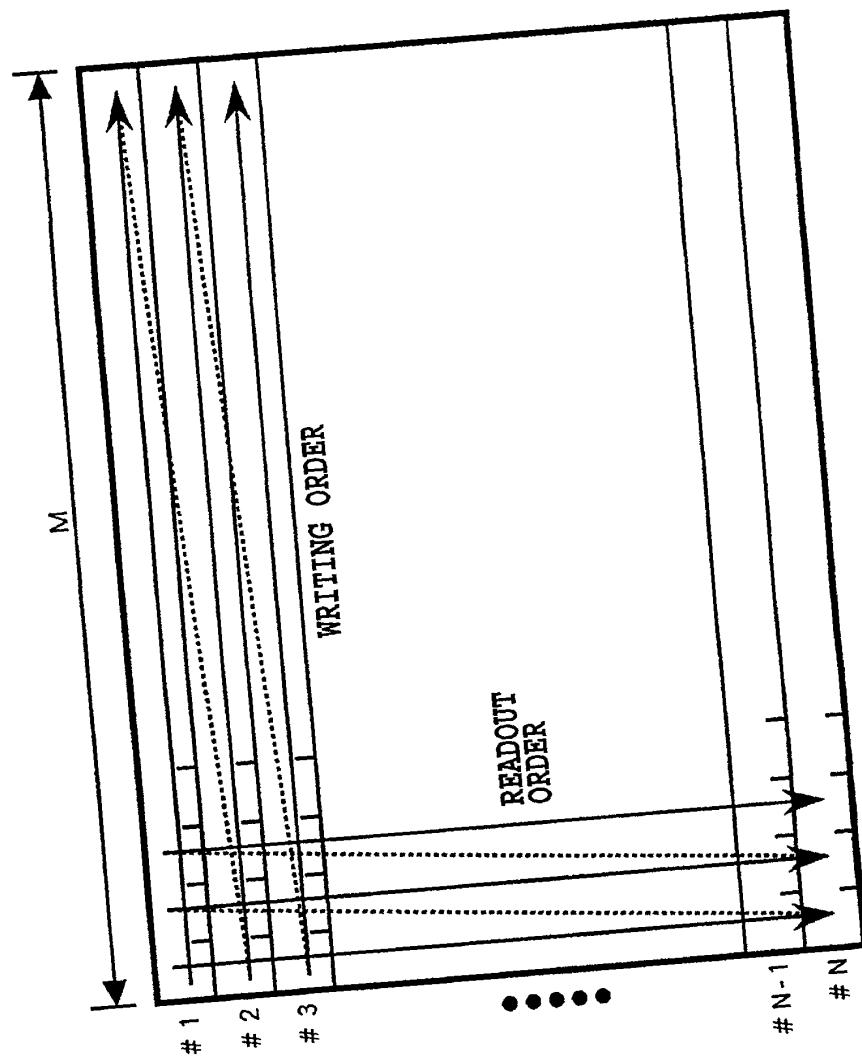


FIG.5

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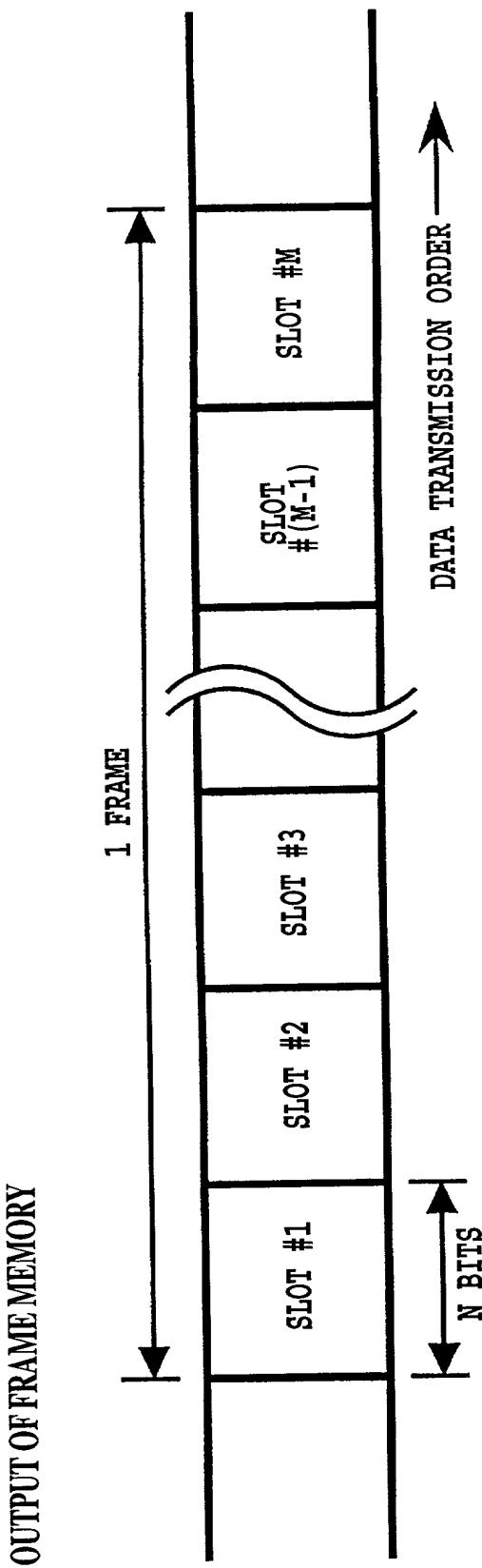


FIG.6

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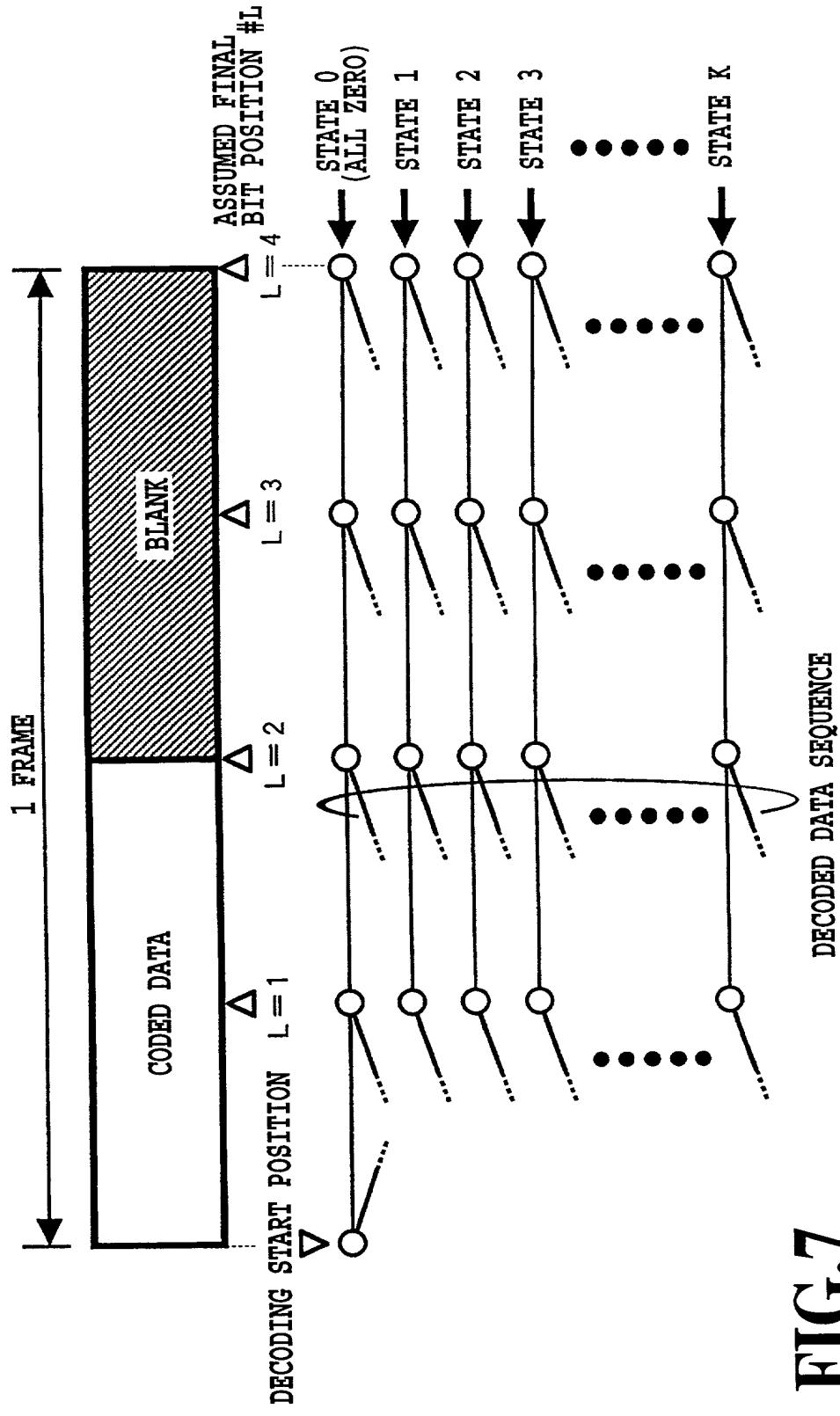


FIG.7

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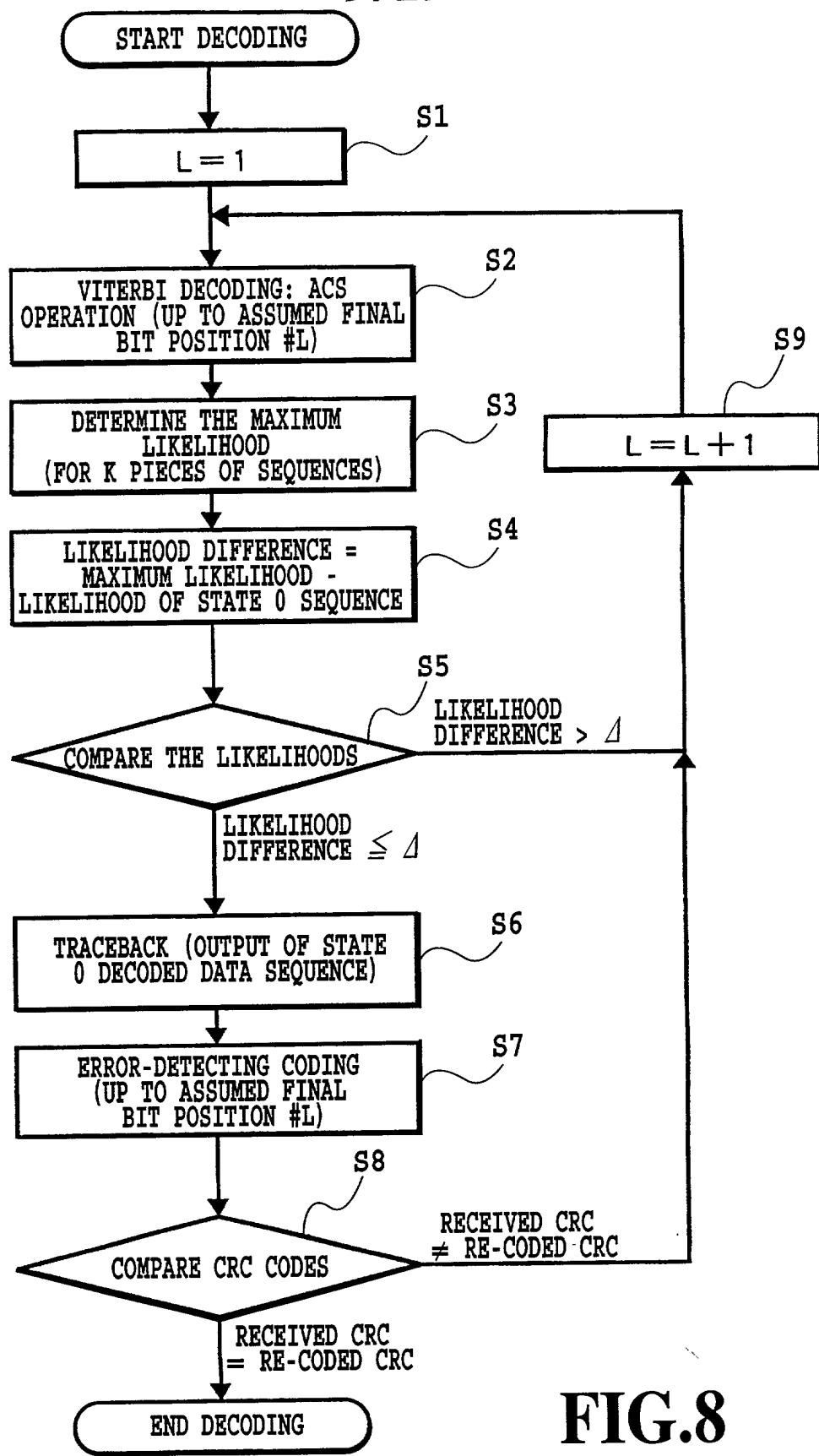


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FIG.9

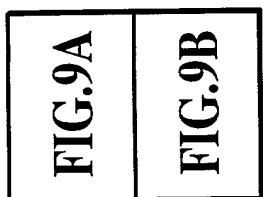
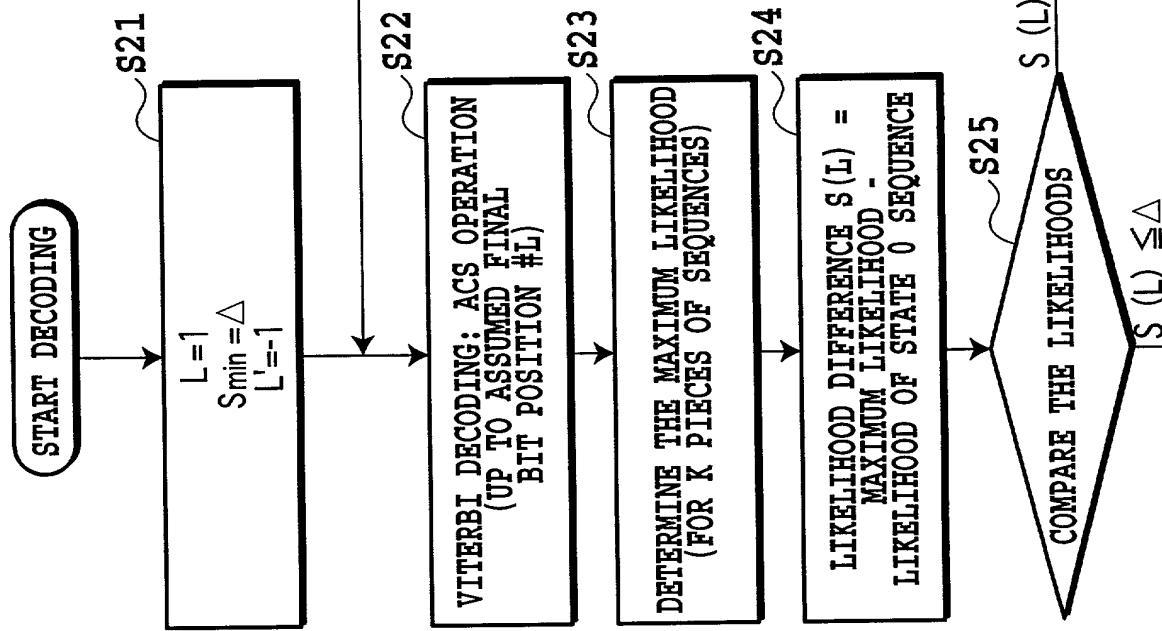
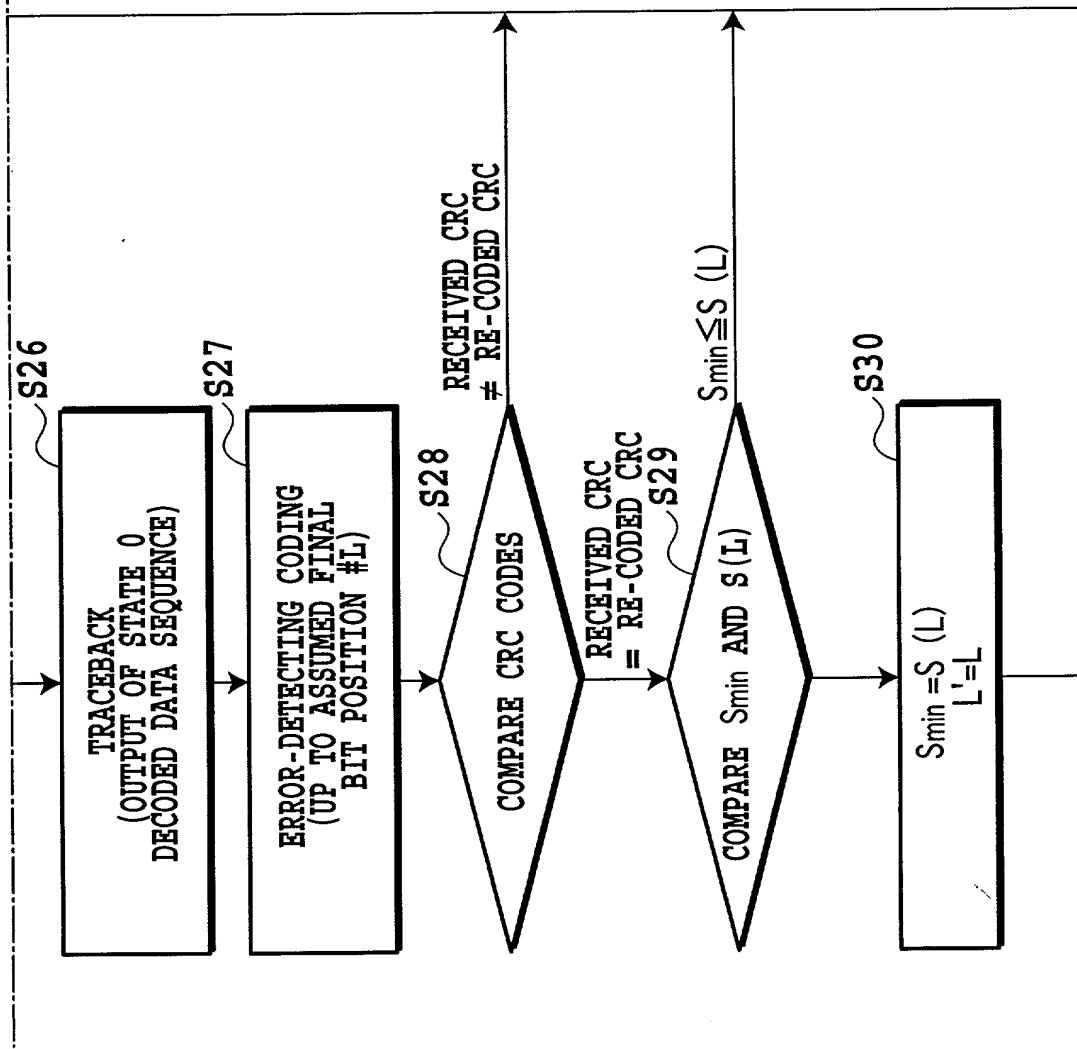


FIG.9A



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FIG.9B



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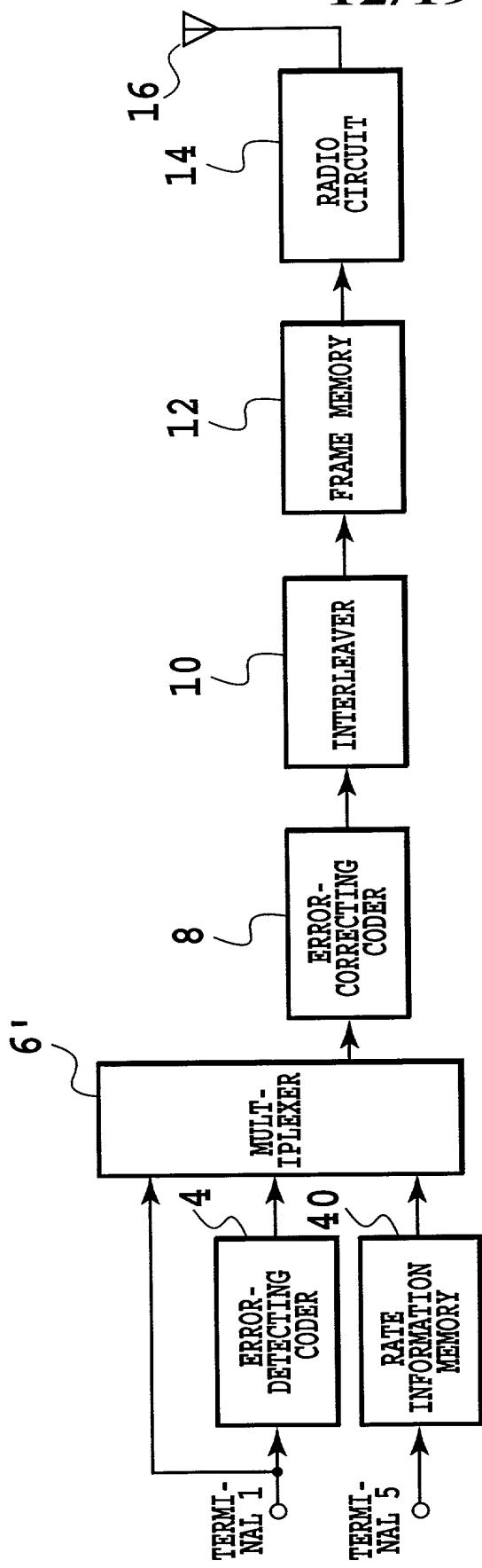


FIG.10A

TRANSMITTER CONFIGURATION

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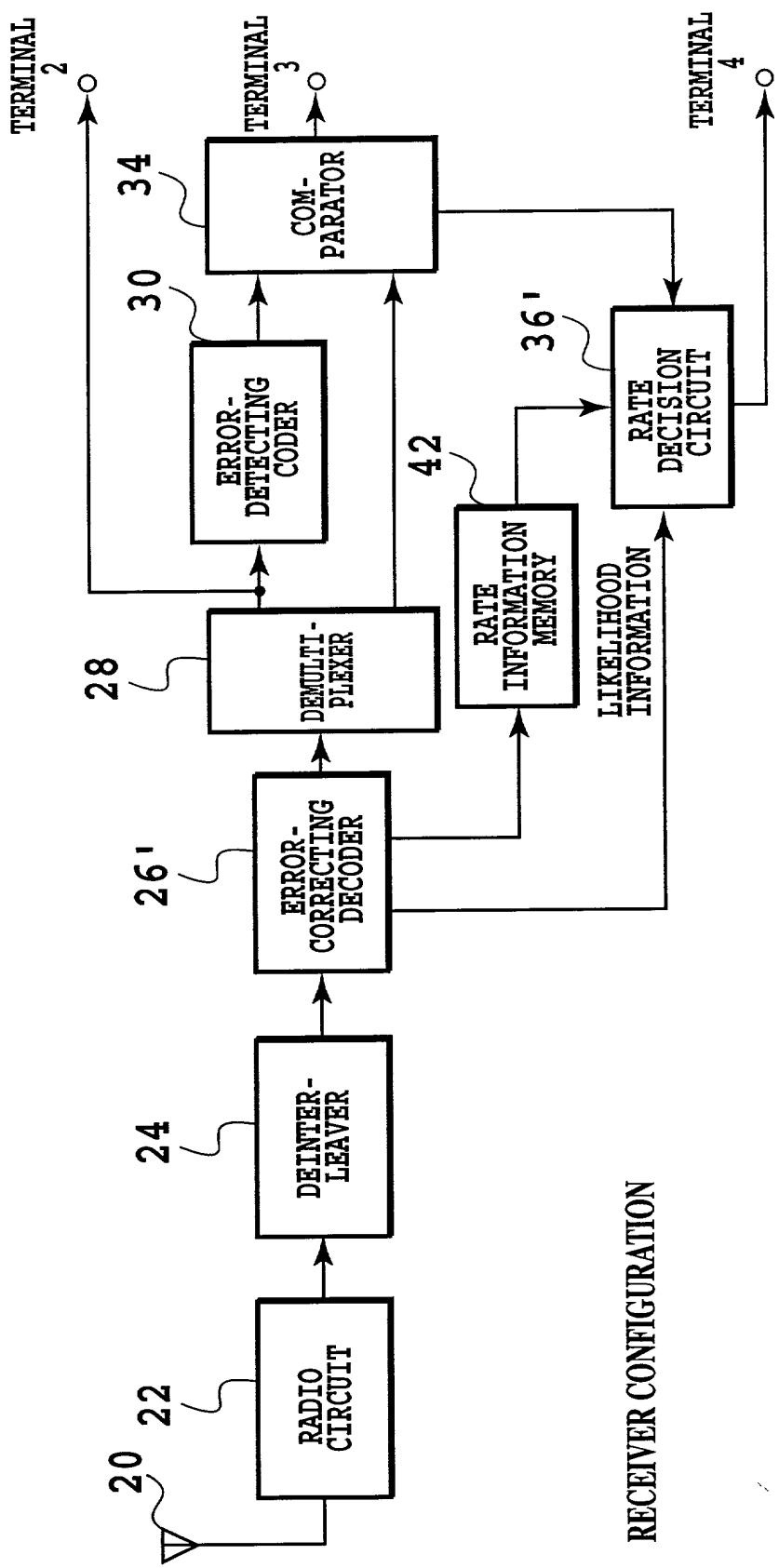


FIG.10B

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OUTPUT OF MULTIPLEXER

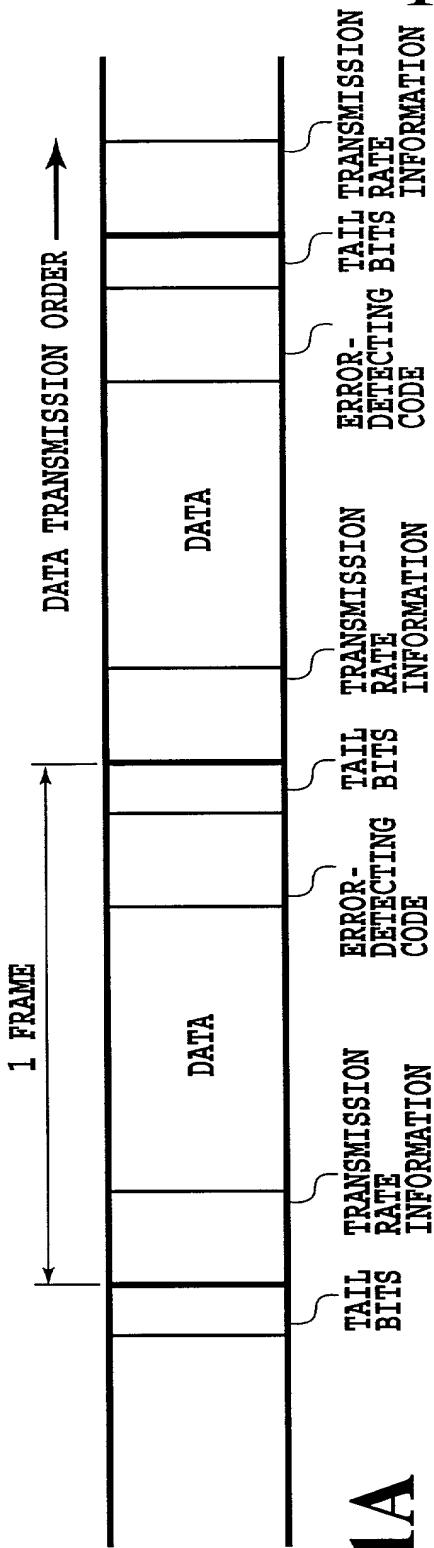


FIG.11A

OUTPUT OF MULTIPLEXER

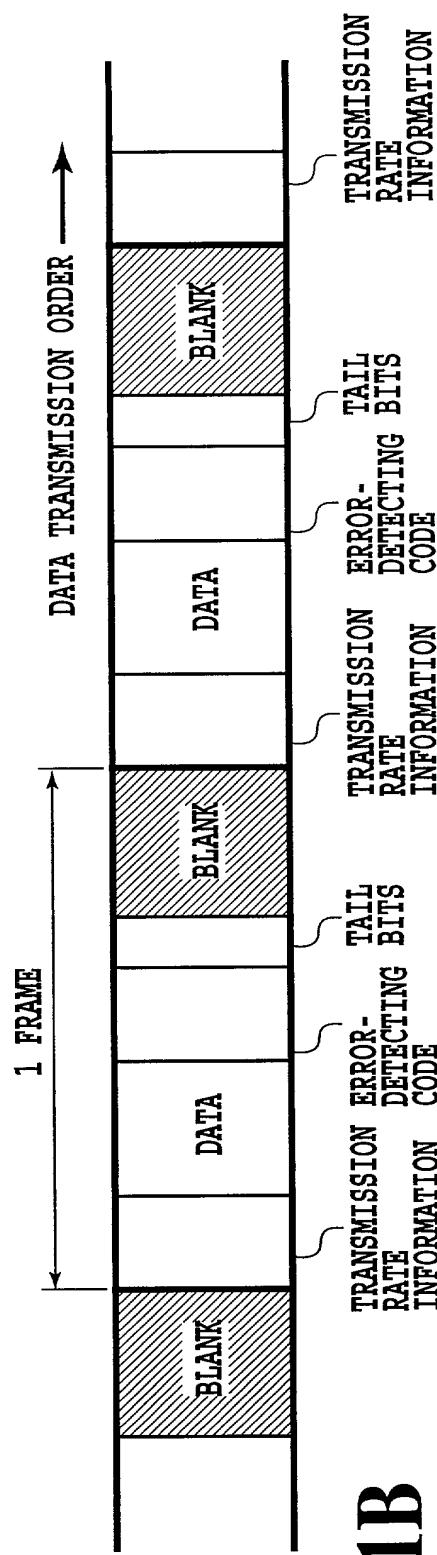


FIG.11B

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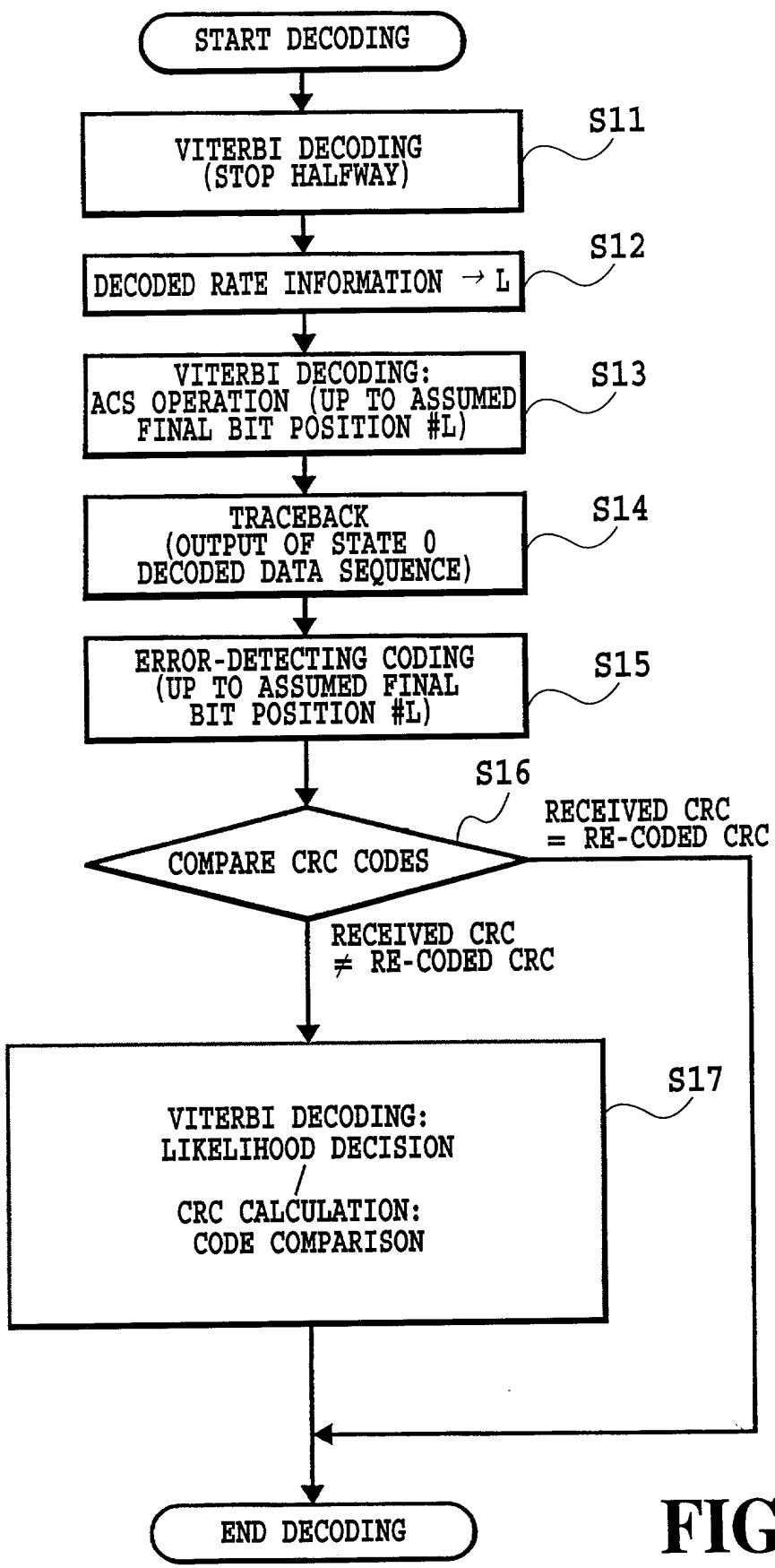
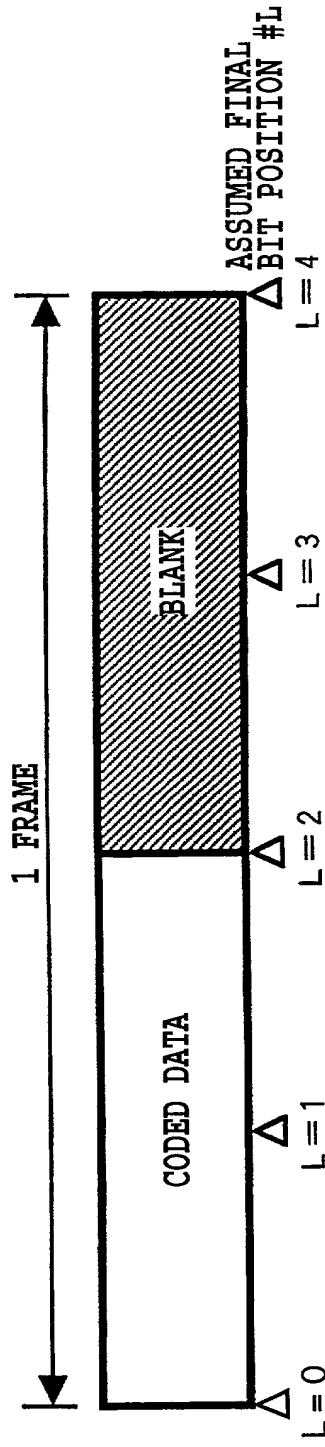
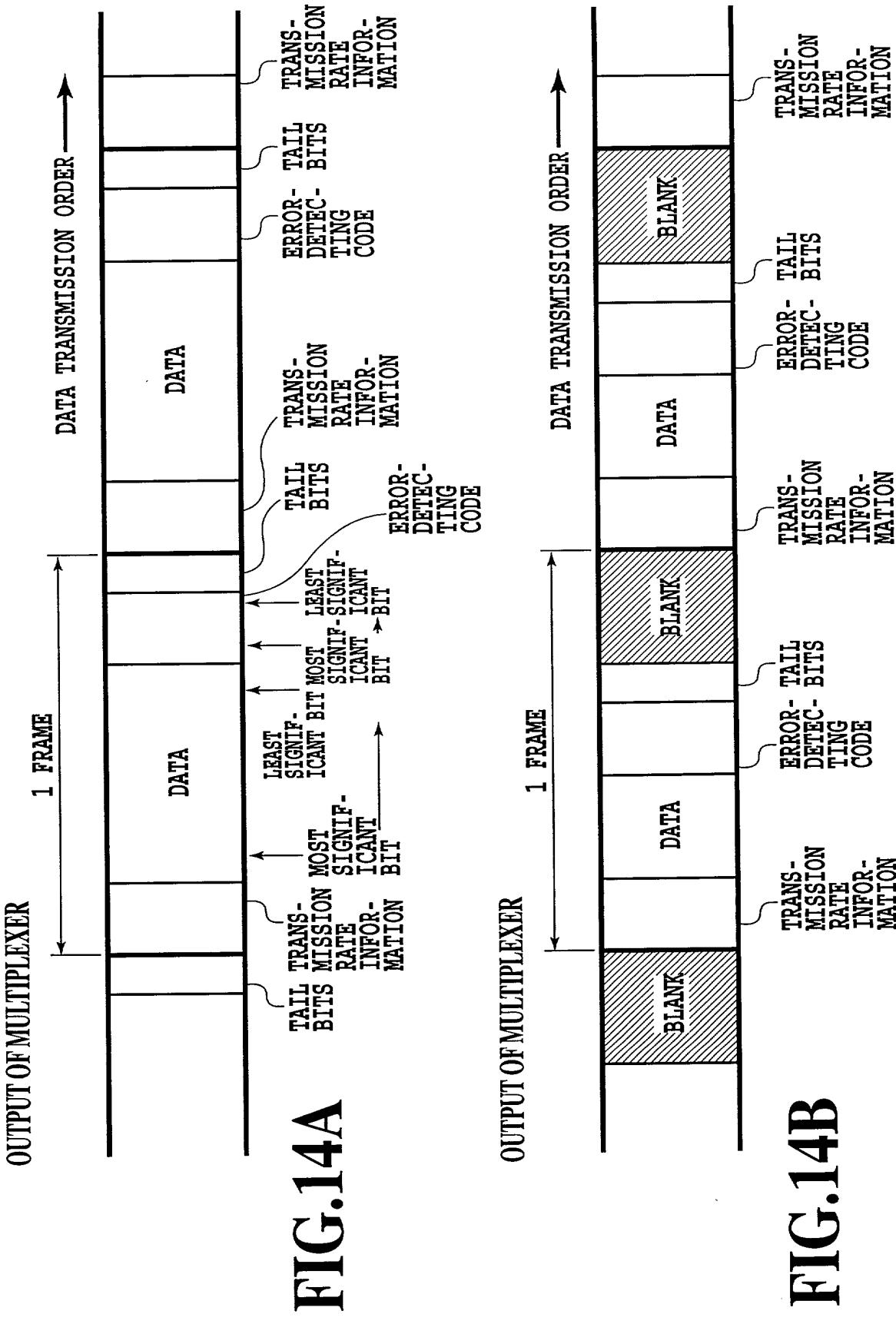


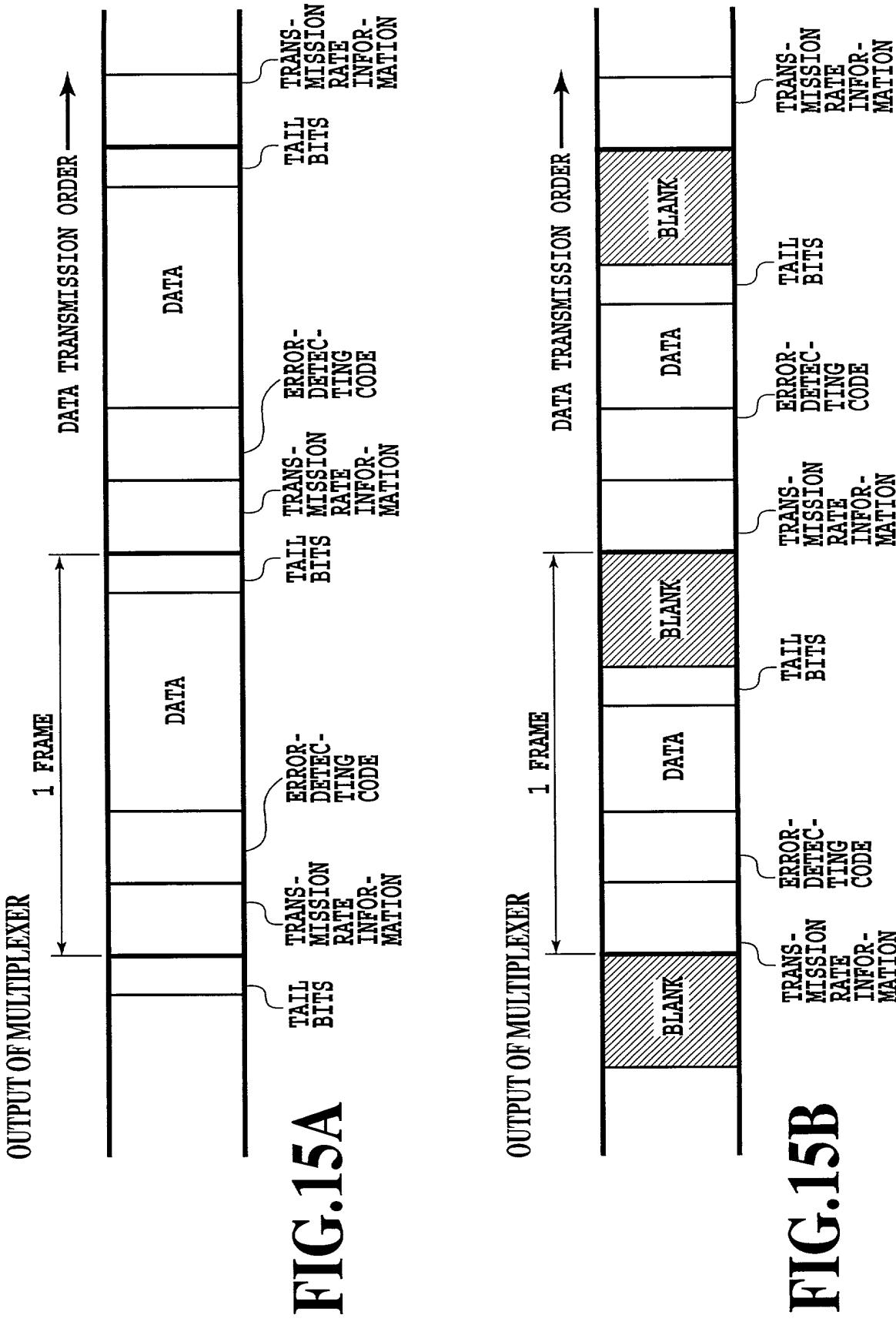
FIG.12

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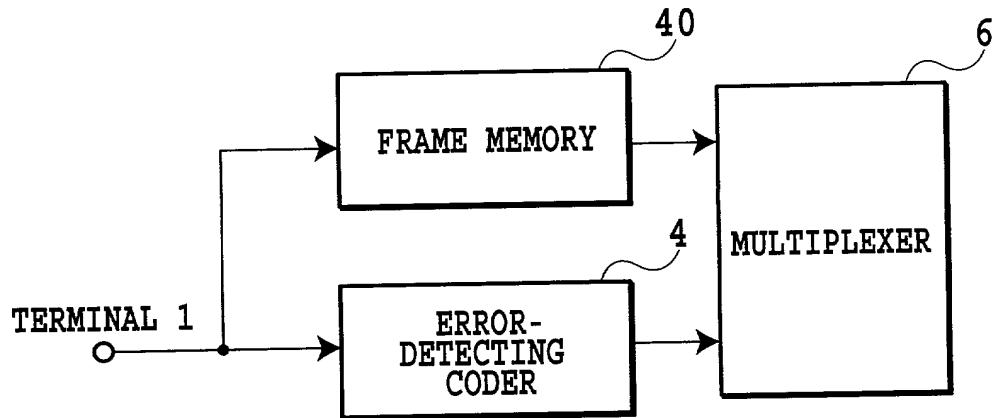
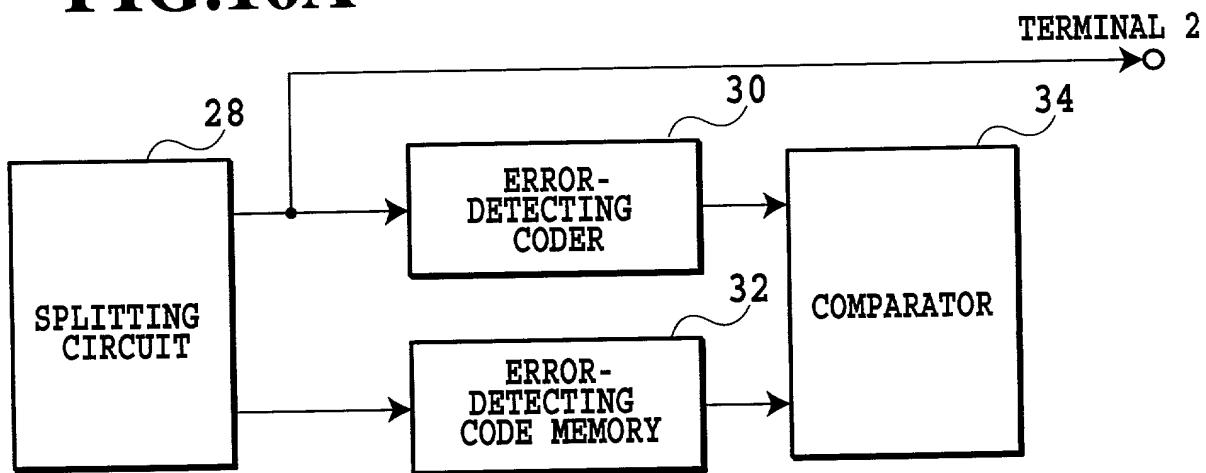
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**FIG.16A****FIG.16B**